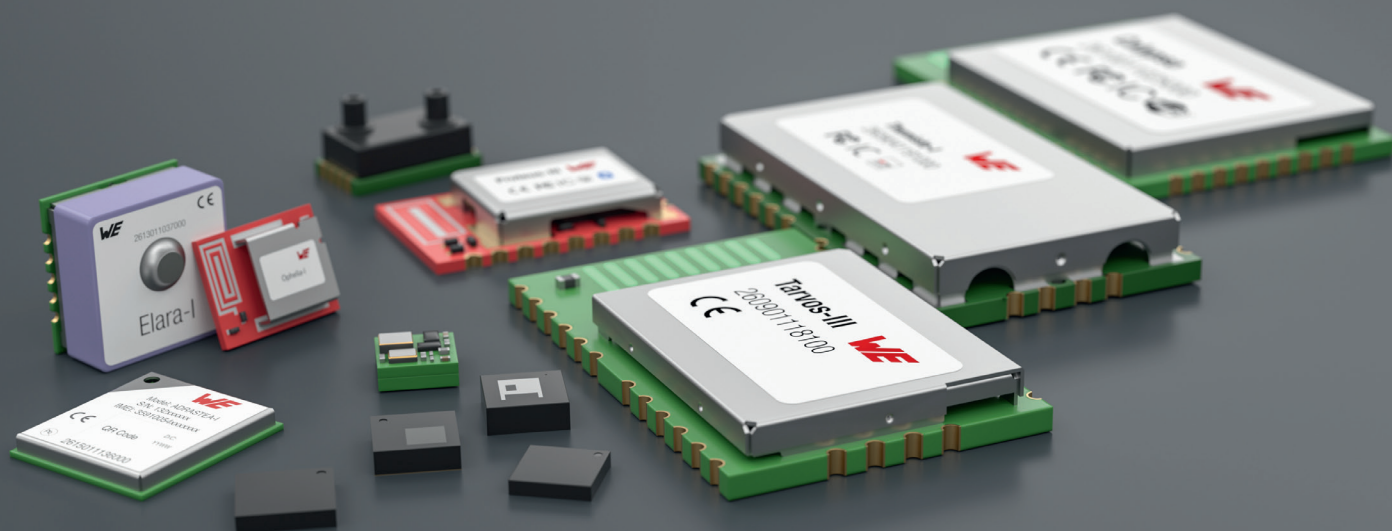




USER MANUAL

ORTHOSIE-I

2617011022000

VERSION 1.2

OCTOBER 30, 2025

WÜRTH ELEKTRONIK MORE THAN YOU EXPECT

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1 Revision history

Manual version	HW version	Notes	Date
1.0	4.0	• Initial version	May 2024
1.1	4.0	• Added application note ANR035 as additional documentation • Updated Flashing information	October 2024
1.2	4.0	• Added references for PC tools and documentation used for chipset flashing and radio module testing	October 2025

Overview of helpful application notes

Application note ANR010 - Range estimation

<http://www.we-online.com/ANR010>

This application note presents the two most used mathematical range estimation models, Friis and two ray ground reflection, and its implementation in the range estimation tool of the RED-EXPERT.

Application note ANR027 - Bluetooth qualification guide

<http://www.we-online.com/ANR027>

Every product containing Bluetooth® technology needs to be qualified at the Bluetooth® SIG (special interest group). This application note explains the steps to be done to gain a Bluetooth® qualification for the end product using a Würth Elektronik eiSos Bluetooth® LE radio module.

Application note ANR031 - Certification of custom modules

<http://www.we-online.com/ANR031>

This application note explains how certifications of a standard product can be used to gain the certification of a customized product. This is done for firmware, which has been adapted by Würth Elektronik eiSos, as well as for firmware written by customer.

Ground plane effects on radio module antennas

<http://www.we-online.com/ANR033>

The ground plane plays a critical role in the performance of radio module antennas, affecting parameters such as radiation pattern, gain, and efficiency. This application note provides practical insights into how ground plane size, shape, and placement influence antenna behavior, offering guidance for optimal integration in real-world designs. Simulation results and measurement data are included to illustrate key effects and support design decisions.

Application note ANR035 - Integration in Raspberry Pi

<http://www.we-online.com/ANR035>

Orthosie-I is compatible with the project *esp-hosted* by Espressif. It allows to use the module as WiFi and Bluetooth® LE interface using Raspberry Pi as a host. The application note ANR035 describes the steps to use the Orthosie-I with the esp-hosted project for easy integration.

Application note ANR036 - Build your own firmware - getting started with Zephyr®

<http://www.we-online.com/ANR036>

This application note describes how to use the Zephyr® to develop firmware for the supported Würth Elektronik eiSos BYOF (build your own firmware) radio modules. This includes the installation of the Zephyr® build system, as well as the selection of an appropriate example for the underlying radio module hardware.

2 Abbreviations

Abbreviation	Name	Description
BYOF	Build Your Own Firmware	Radio module without firmware to develop custom firmware
EV (Board)	Evaluation (Board)	Orthosie-I populated on motherboard with USB interface for test and evaluation purposes
MCU	MicroController Unit	
RF	Radio Frequency	Describes everything relating to the wireless transmission
SDK	Software Development Kit	Set of tools for third-party developers to use in producing applications using a particular framework or platform
UART	Universal Asynchronous Receiver Transmitter	Protocol for the exchange of data in series between two devices
VDD	Voltage Drain Drain	Supply voltage

3 Introduction

3.1 Operational description

The Orthosie-I module is a radio module/device for wireless communication between devices such as control systems, remote controls, sensors etc.



Be aware that the Orthosie-I module contains a test firmware version exclusively used for our production process.

The user has the complete freedom to develop his own application based on the Espressif SDKs [1] (e.g. Bluetooth® LE, WiFi, Matter). To fulfil the needs and specifications of such applications, a tailored firmware can be developed, based on the Orthosie-I hardware. This includes the connection and communication to custom sensors, custom Bluetooth® LE profiles, timing configurations, security configurations as well as power consumption optimizations. Even with it's small dimensions of 9.5 mm x 13 mm, Orthosie-I provides a strongly miniaturized integrated PCB antenna.

The main functionality is accessible through pads underneath the radio module.



The Orthosie-I shares the same hardware platform as the Stephano-I module. For this reason, Stephano-I is often referred to in this user manual.



Figure 1: Orthosie-I

3.2 Block diagram

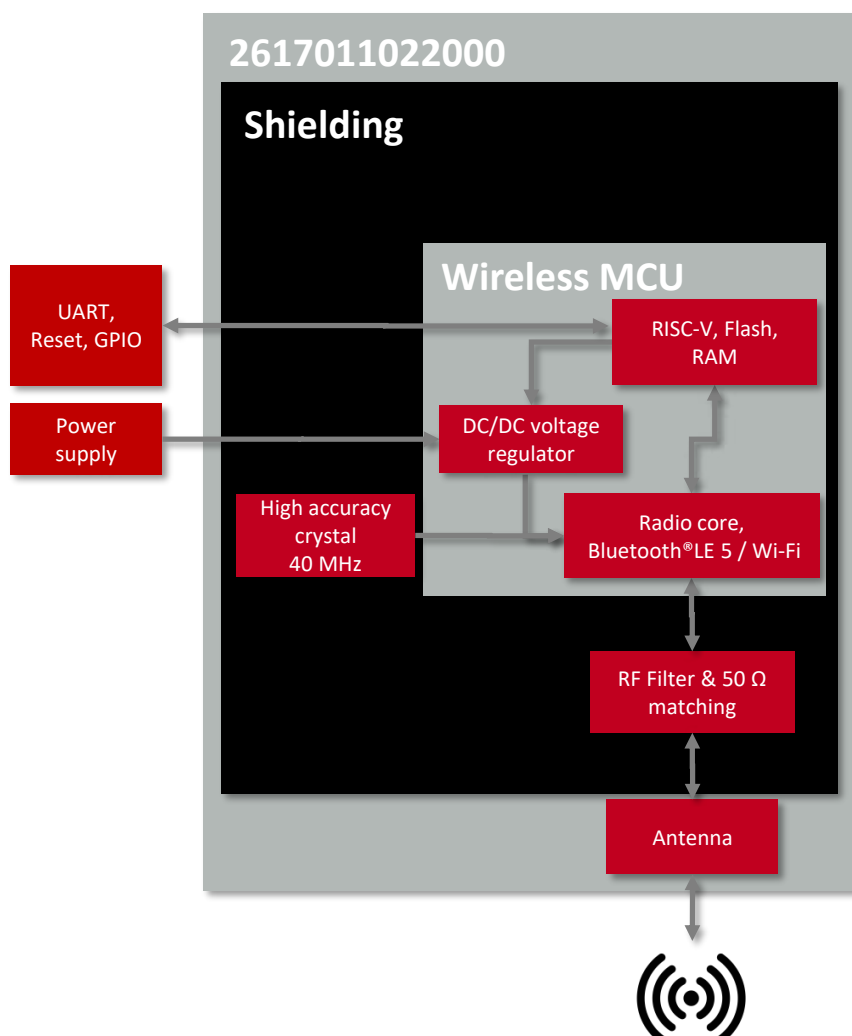


Figure 2: Block diagram

3.3 Ordering information

WE order code	Description
2617011022000	Orthosie-I BYOF module with integrated PCB antenna, Tape & Reel
2617029022001	Evaluation board with Orthosie-I module mounted

Table 2: Ordering information

4 Electrical specifications

If not otherwise stated, measured on the Orthosie-I evaluation board with $T = 25\text{ °C}$, $V_{DD} = 3.3\text{ V}$.

4.1 Operating conditions

Description	Min.	Typ.	Max.	Unit
VDD	3.0	3.3	3.6	V
Temperature range	-40		85	°C

Table 3: Operating conditions

4.2 Absolute maximum ratings

Description	Min.	Typ.	Max.	Unit
VDD	-0.3		3.6	V

Table 4: Absolute maximum ratings

4.3 Power consumption

4.3.1 Static

Description	Test condition	Min.	Typ.	Max.	Unit
WiFi TX current consumption at max output power	Mode: WiFi 11b, Data rate: 1 Mbps, Power index: 80		167		mA
WiFi RX current consumption			82		mA
Bluetooth® LE TX current consumption at max output power	Mode: Bluetooth® LE, Data rate: 1 Mbps, Power index: 12		161		mA
Bluetooth® LE RX current consumption			81		mA
Deep-sleep mode		5			µA
System-off mode (/RESET set to GND)		1			µA

Table 5: Power consumption

4.4 Radio characteristics

Description	Test condition	Min.	Typ.	Max.	Unit
Max output power ¹	Data rate: 1 Mbps, Power index: 80		13.4		dBm
Input sensitivity	Data rate: 1 Mbps		-87		dBm
Frequencies		2412		2484	MHz

Table 6: WiFi radio characteristics (radiated)

Description	Test condition	Min	Typ.	Max	Unit
Max output power ¹	Data rate: 1 Mbps, Power index: 12		4.5		dBm
Input sensitivity	Data rate: 1 Mbps		-89		dBm
Frequencies		2402		2480	MHz

Table 7: Bluetooth® LE radio characteristics (radiated)

4.5 Pin characteristics

Property	Min.	Typ.	Max.	Unit
Pin input low voltage	-0.3		0.25×VDD	V
Pin input high voltage	0.75×VDD		VDD+0.3	V
Pin output low voltage			0.1×VDD	V
Pin output high voltage	0.8×VDD			V
Pin output current sunk by any I/O and control pin		40		mA
Pin output current sourced by any I/O and control pin		28		mA
Internal pull-up/pull-down resistor		45		kΩ

Table 8: Pin characteristics

¹ Refer to the Table 10 for the certified power settings. For the certification of the end device, the power index can be adjusted to achieve the maximum certifiable output power.

5 Pinout

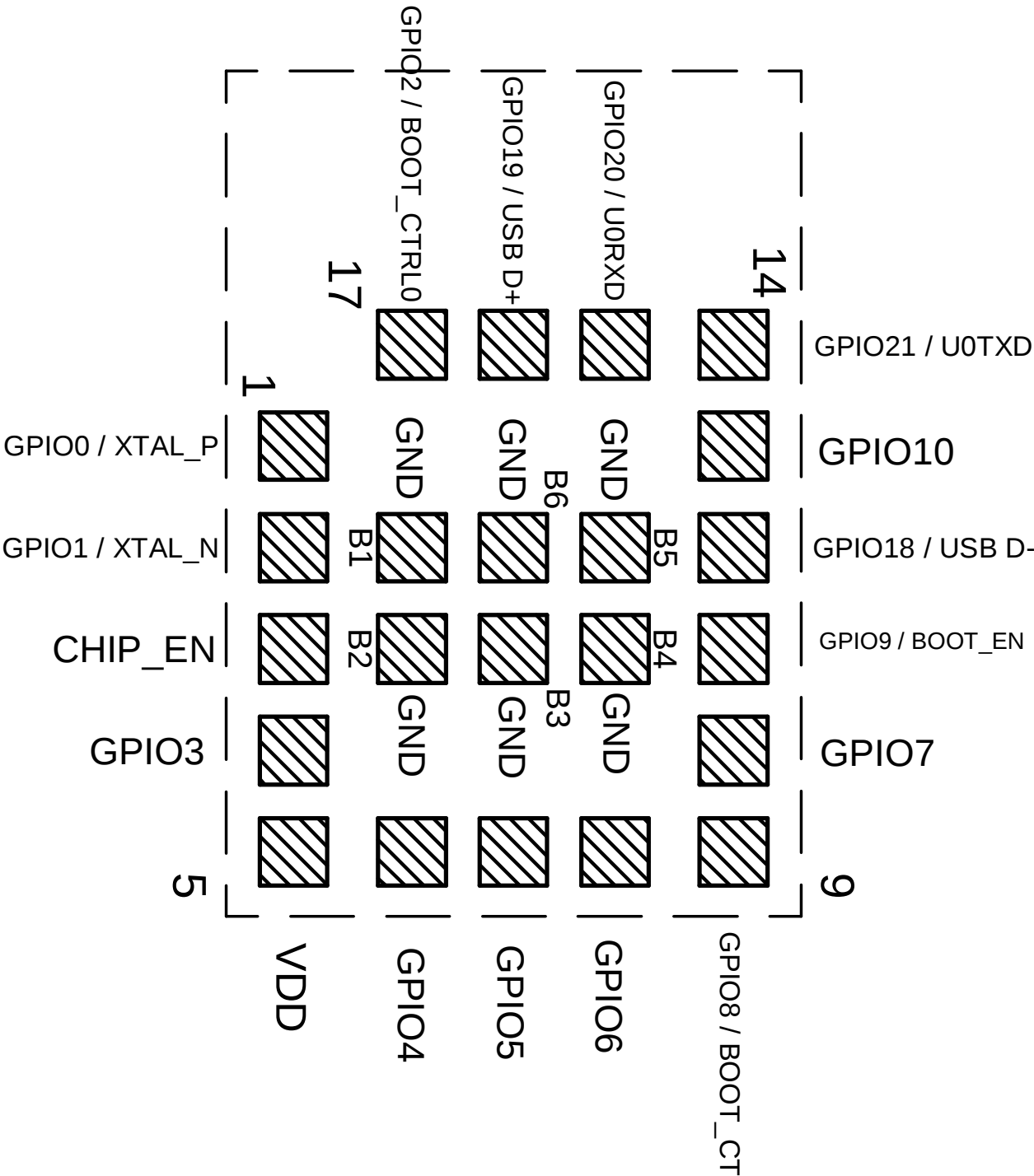


Figure 3: Pinout (top view)

No	µC Pin	I/O	Description
1	<i>GPIO0 / XTAL_P²</i>	I/O	General purpose input / output.
2	<i>GPIO1 / XTAL_N²</i>	I/O	General purpose input / output.
3	<i>CHIP_EN</i>	Input	Reset pin. A low signal resets the module.
4	<i>GPIO3</i>	I/O	General purpose input / output.
5	<i>VDD</i>	Supply	Supply voltage
6	<i>GPIO4</i>	I/O	General purpose input / output.
7	<i>GPIO5</i>	I/O	General purpose input / output.
8	<i>GPIO6</i>	I/O	General purpose input / output.
9	<i>GPIO8 / BOOT_CTRL1</i>	Input	Boot control pin. External 1.5 kΩ pull-up resistor needed.
10	<i>GPIO7</i>	I/O	General purpose input / output.
11	<i>GPIO9 / BOOT_EN</i>	Input	Boot control pin. Low level: Boot Mode. High level: Application mode. Uses internal pull-up ¹
12	<i>GPIO18 / USB D-³</i>	I/O	General purpose input / output.
13	<i>GPIO10</i>	I/O	General purpose input / output.
14	<i>GPIO21 / U0TXD</i>	Output	Debug UART (Transmission). Do not connect if not needed.
15	<i>GPIO20 / U0RXD</i>	Input	Debug UART (Reception). Uses internal pull-up resistor ¹ . Do not connect if not needed.
16	<i>GPIO19 / USB D+³</i>	I/O	General purpose input / output.
17	<i>GPIO2 / BOOT_CTRL0</i>	Input	Boot control pin. External 1.5 kΩ pull-up resistor needed.
B1 - B6	<i>GND</i>	Supply	Ground

Table 9: Pinout



The debug UART pin as well as the boot pins are used for programming/debugging the radio chip. During execution of the firmware, and in case no UART debug messages are used, these pins can be reconfigured and used as normal GPIOs.

¹ Internal pull-ups or pull-downs (45 kΩ) are configured at start-up by the firmware installed in the SoC.

² Pins available to connect an external crystal.

³ Pins available to connect a USB.

6 Certification of the end device

For the certification of the end device, which integrates the Orthosie-I, it is necessary to check the Orthosie-I's radio behaviour.

To do that, a special test firmware¹ that must be programmed using the "Espressif Flash Download Tool" [2, 3] and the debug UART (UART0) of the radio module. All Espressif PC tools need to access the debug UART (UART0) and it's control pins in the same way. Please refer to chapter 8.3 for more information on that.

As soon as the test firmware is available on the Orthosie-I, the "Espressif RF Test Tool" is used to control the radio test modes of the Orthosie-I via the debug UART (UART0) interface. For more details with respect to the "Espressif RF Test Tool", please refer to the "Espressif Test Guide" [4].

6.1 Radio power settings

The radio module Stephano-I (variant of Orthosie-I with firmware for Bluetooth® LE and WiFi support) has been certified using Bluetooth® LE and WiFi radio. It has passed the certification tests for several countries with the maximum output powers as stated below:

Country	Maximum Bluetooth® LE power index	Maximum WiFi power index
TELEC/Japan	12 (9 dBm)	74 (18.5 dBm)
RED/EU	13 (12 dBm)	80 (20 dBm)
FCC/US	13 (12 dBm)	80 (20 dBm)
IC/Canada	13 (12 dBm)	80 (20 dBm)

Table 10: Maximum allowed power setting



Note that the power index only defines the conducted output power of the radio chip. The actual output power radiated by the module's integrated antenna is lower due to the antenna loss. Refer to chapter Electrical specifications for more details about the radiated output power.

¹The test firmware is part of the "Espressif RF Test Tool" download package.

7 Design in guide

7.1 Advice for schematic and layout

For users with less RF experience it is advisable to closely copy the relating EV-Board with respect to schematic and layout, as it is a proven design. The layout should be conducted with particular care, because even small deficiencies could affect the radio performance and its range or even the conformity.

The following general advice should be taken into consideration:

- A clean, stable power supply is strongly recommended. Interference, especially oscillation can severely restrain range and conformity.
- Variations in voltage level should be avoided.
- LDOs, properly designed in, usually deliver a proper regulated voltage.
- Blocking capacitors and a ferrite bead in the power supply line can be included to filter and smoothen the supply voltage when necessary.
- Elements for ESD protection should be placed on all pins that are accessible from the outside and should be placed close to the accessible area. For example, the RF-pin is accessible when using an external antenna and should be protected.
- ESD protection for the antenna connection must be chosen such as to have a minimum effect on the RF signal. For example, a protection diode with low capacitance such as the 8231606A or a 68 nH air-core coil connecting the RF-line to ground give good results.
- Placeholders for optional antenna matching or additional filtering are recommended.
- The antenna path should be kept as short as possible.
- The use of an external reset IC should be considered if one of the following points is relevant:
 - The slew rate of the power supply exceeds the electrical specifications.
 - The effect of different current consumptions on the voltage level of batteries or voltage regulators should be considered. The module draws higher currents in certain scenarios like start-up or radio transmit which may lead to a voltage drop on the supply. A restart under such circumstances should be prevented by ensuring that the supply voltage does not drop below the minimum specifications.
 - Voltage levels below the minimum recommended voltage level may lead to malfunction. The reset pin of the module shall be held on LOW logic level whenever the VDD is not stable or below the minimum operating Voltage.
 - Special care must be taken in case of battery powered systems.
- To avoid the risk of short circuits and interference there should be no routing underneath the module on the top layer of the baseboard.
- On the second layer, a ground plane is recommended, to provide good grounding and shielding to any following layers and application environment.

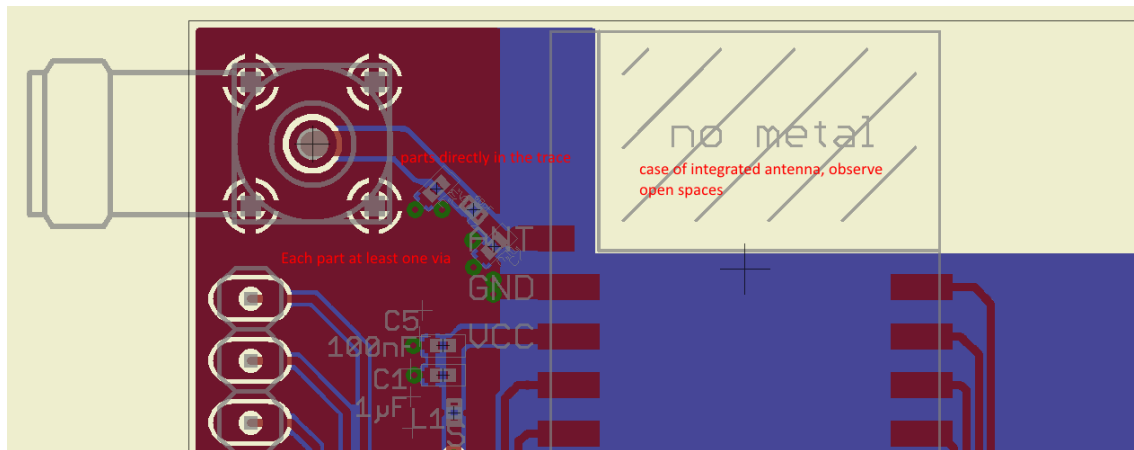


Figure 4: Layout

- In case of integrated antennas it is required to have areas free from ground. This area should be copied from the EV-Board.
- The area with the integrated antenna must overlap with the carrier board and should not protrude, as it is matched to sitting directly on top of a PCB.
- Modules with integrated antennas should be placed with the antenna at the edge of the main board. It should not be placed in the middle of the main board or far away from the edge. This is to avoid tracks beside the antenna.
- Filter and blocking capacitors should be placed directly in the tracks without stubs, to achieve the best effect.
- Antenna matching elements should be placed close to the antenna / connector, blocking capacitors close to the module.
- Ground connections for the module and the capacitors should be kept as short as possible and with at least one separate through hole connection to the ground layer.
- ESD protection elements should be placed as close as possible to the exposed areas.



Fixed values can not be recommended, as these depend on the circumstances of the application (main power source, interferences etc.).

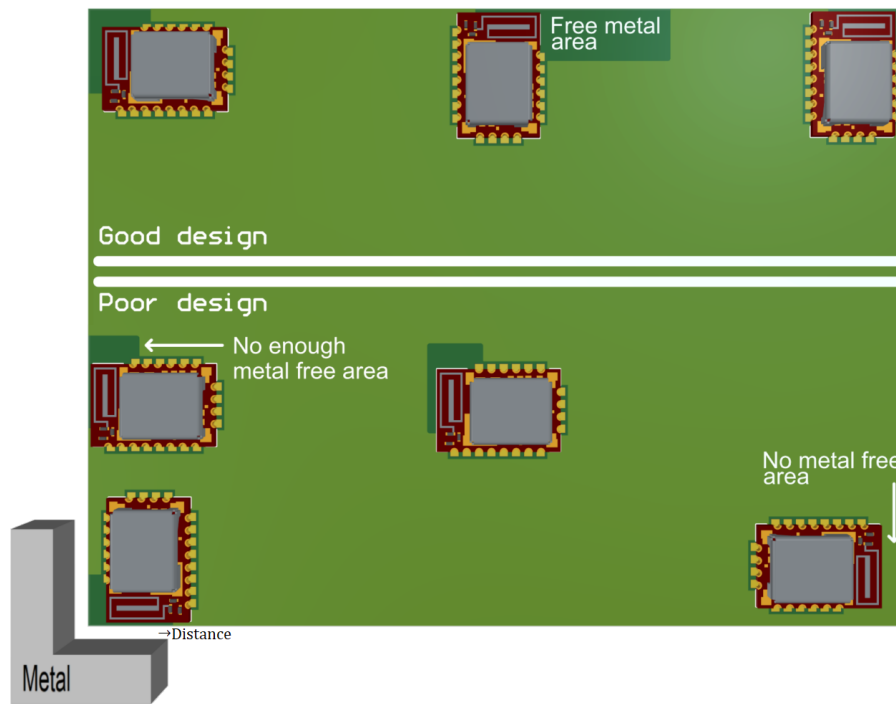


Figure 5: Placement of the module with integrated antenna

8 Reference design

Orthosie-I was tested and certified on the corresponding Orthosie-I evaluation board (order code 2617029022000). For the compliance with the EU directive 2014/53/EU Annex I, the evaluation board serves as reference design.

This is no discrepancy due to the fact that the evaluation board itself does not fall within the scope of the EU directive 2014/53/EU Annex I, as the module is tested on the evaluation board, which is also the recommended use.

Further information concerning the use of the evaluation board can be found in the manual of the Orthosie-I evaluation board.



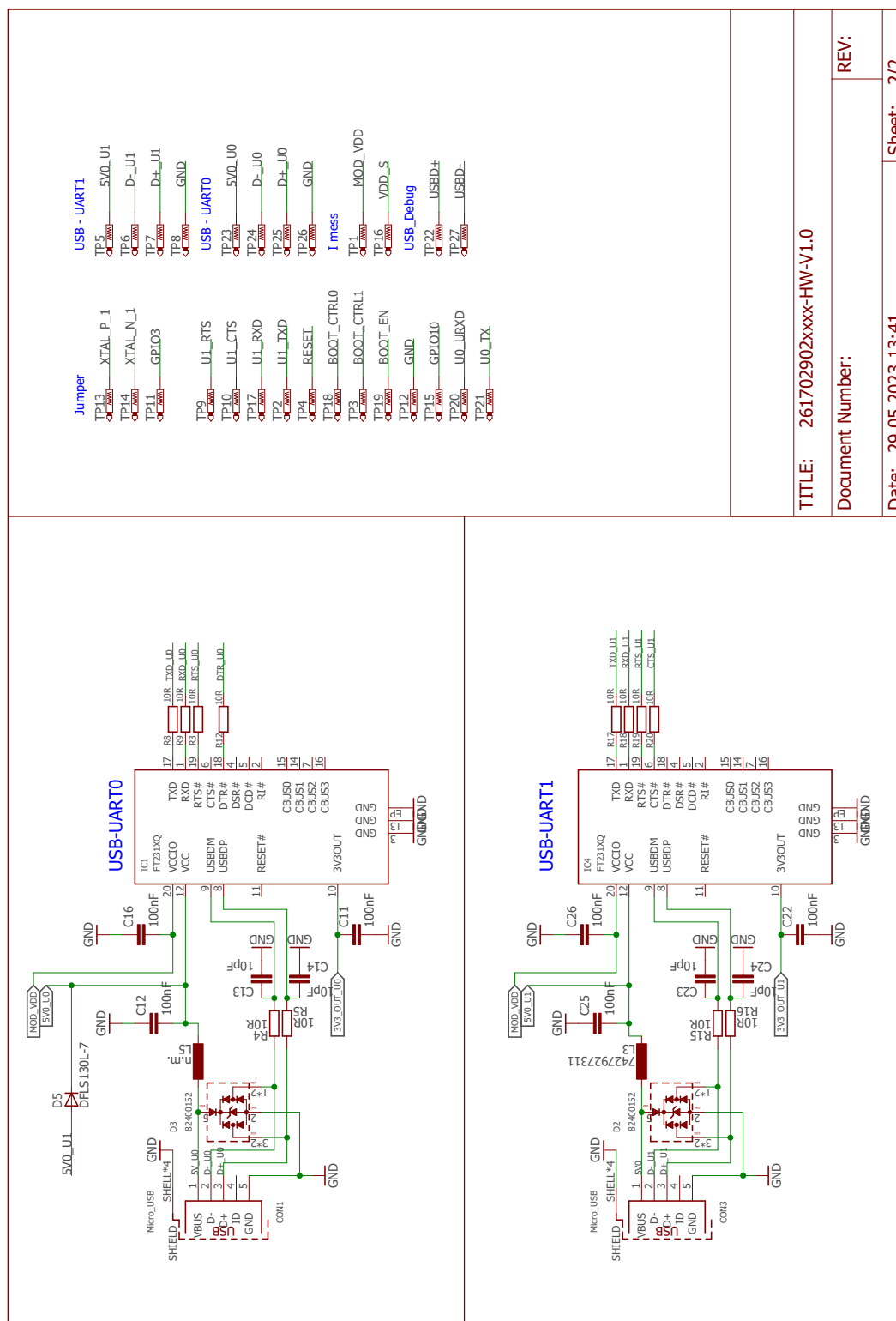


Figure 7: Reference design: schematic page 2

8.2 Layout

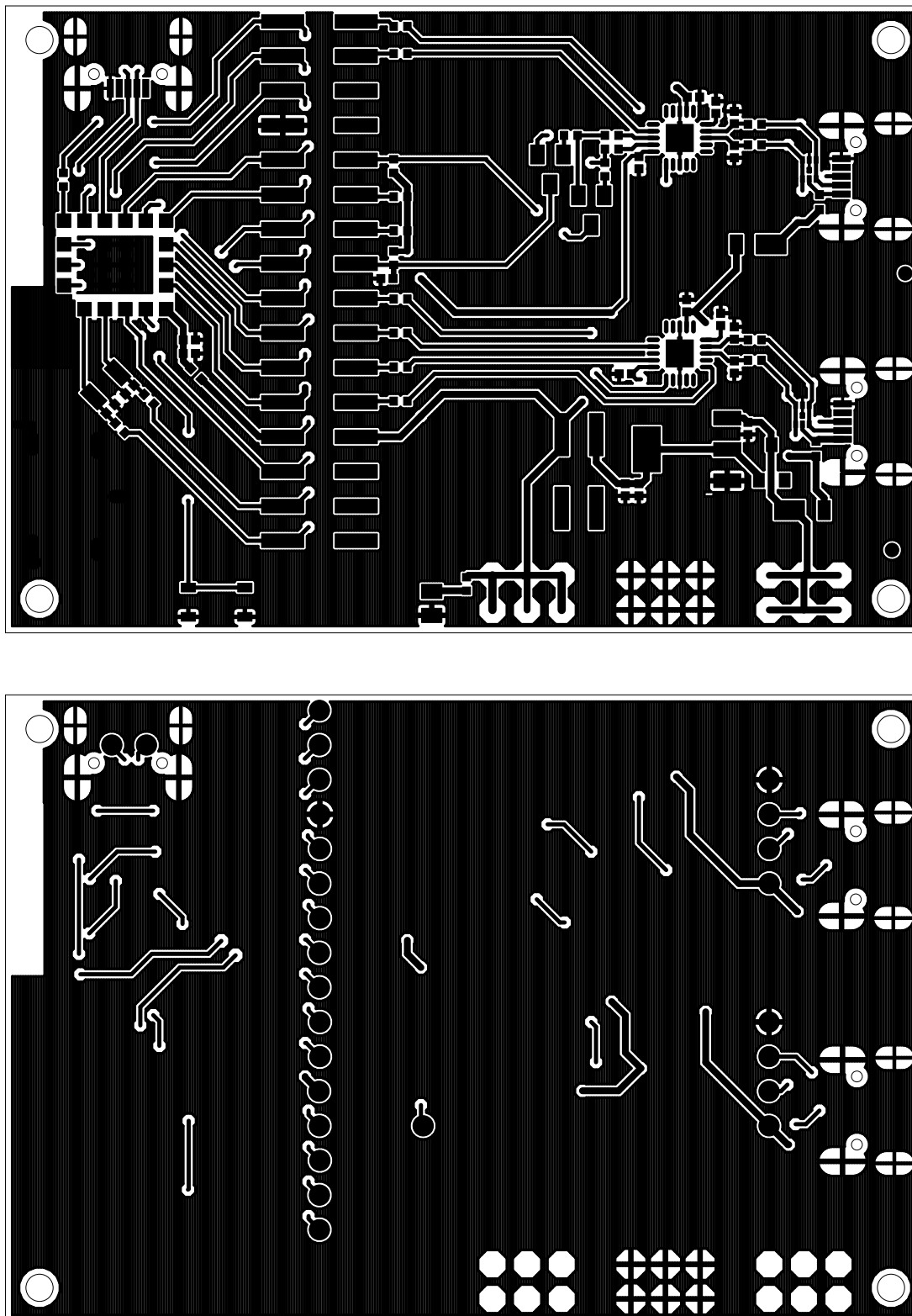


Figure 8: Top layer (top), bottom layer (bottom)

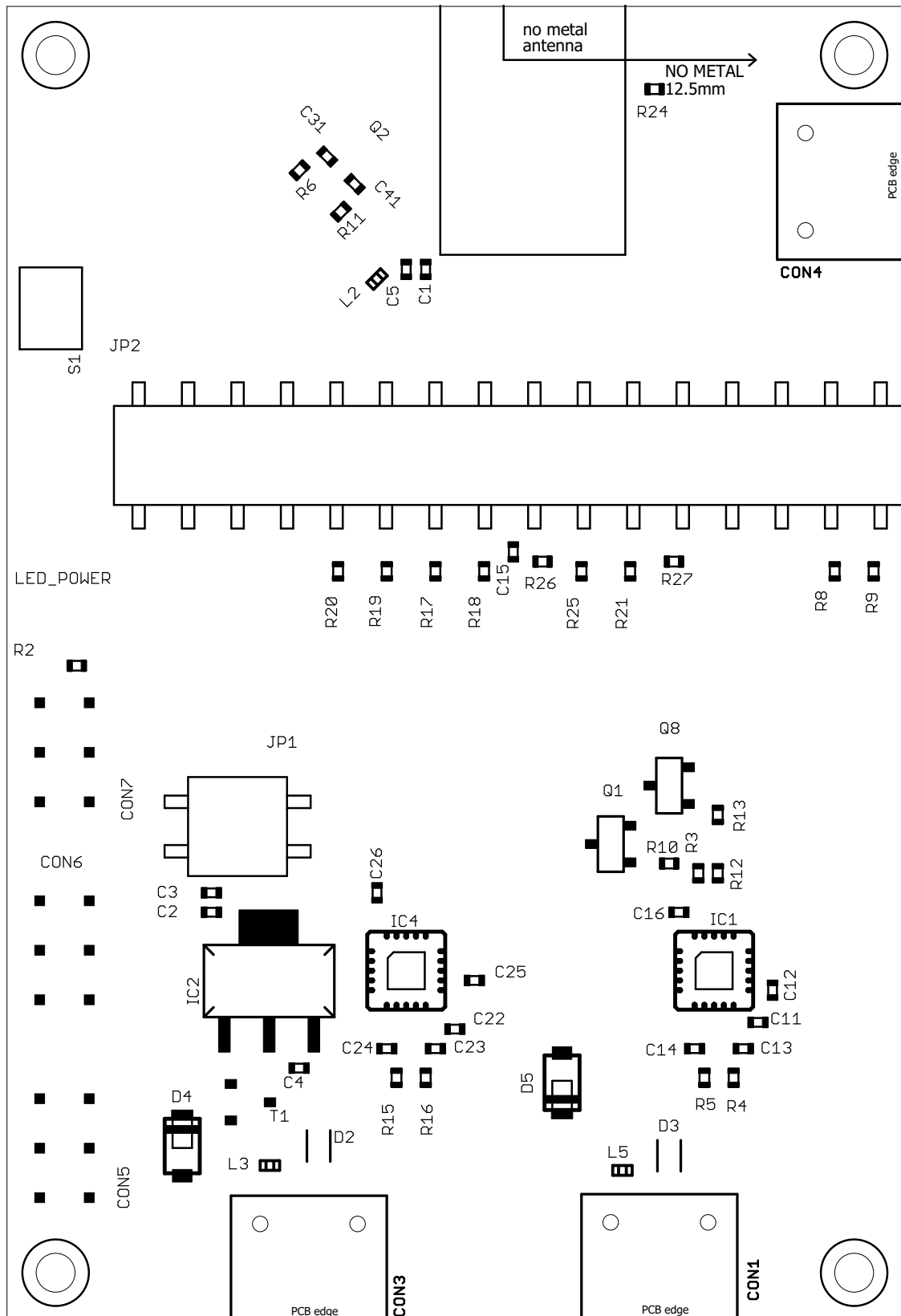


Figure 9: Reference design assembly plan

8.3 Flashing or erasing of the chipset

The debug UART (UART0) of the Orthosie-I can be used to bring the certification or qualification test firmware on the module and to control its test modes.

To be able to program a firmware on the module, the radio module must be set to bootloader mode first. To enter this mode, the *BOOT_EN* pin must be statically set to LOW before a reset is done, by applying a temporary LOW signal to the */RESET* pin. If the bootloader mode has been entered, Espressif PC tools use the pins *U0TXD* and *U0RXD* of the debug UART to program the new image on the radio module.

After the firmware has been programmed on the chipset, the bootloader mode must be left by applying a HIGH level to the *BOOT_EN* pin and resetting the chipset.

In case a special electronic circuit (see figure 10), consisting of transistors and resistors, is connected to the debug UART of the chipset, Espressif PC tools are also able to automatically enter or leave the bootloader mode via the *BOOT_EN* and */RESET* pins.

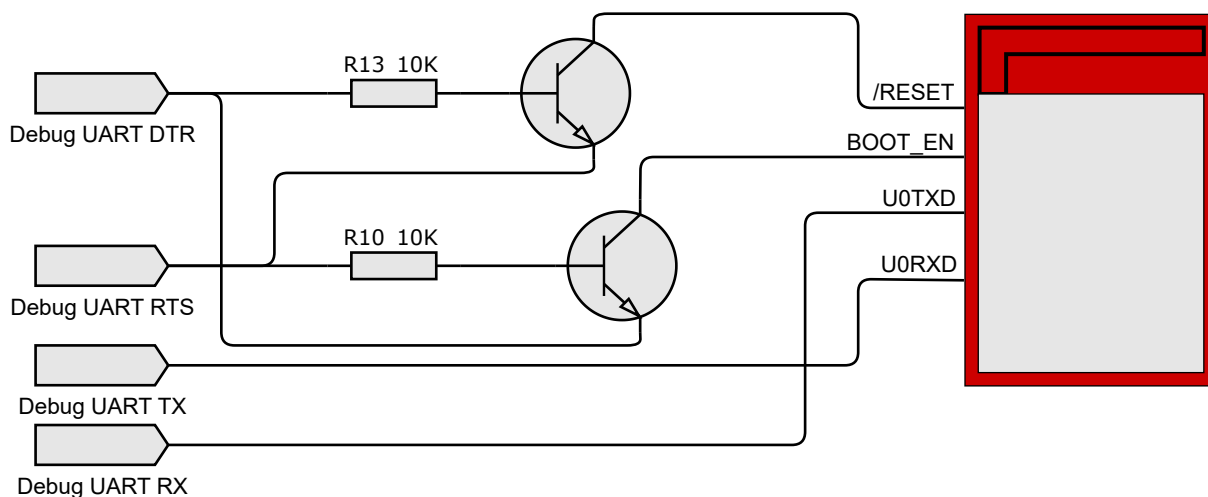


Figure 10: Flash circuit

Having the debug UART interface accessible allows to erase or flash any kind of firmware on the module using the so called "Espressif Flash Download Tool" [2, 3].



Würth Elektronik eiSos cannot guarantee or influence function and behaviour of third party Software. Using third party tools always happens on your own risk.

The designer of an end product shall prevent their end users to use this interface and they shall implement physical protection against access to this interface if required or recommended by the Cybersecurity risk assessment.

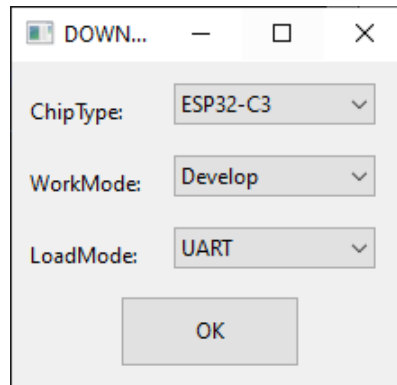


Figure 11: Flash download tool: Chip selection

After start-up of the tool, the selection of the ESP32-C3 chipset, firmware image, start address and the COM port of the debug UART, the radio module can be erased or flashed with a new firmware image.



Note that, in case the flash circuit is not connected to the UART of the module, the *BOOT_EN* and */RESET* must be switched manually, before any action can be done in the PC tool.

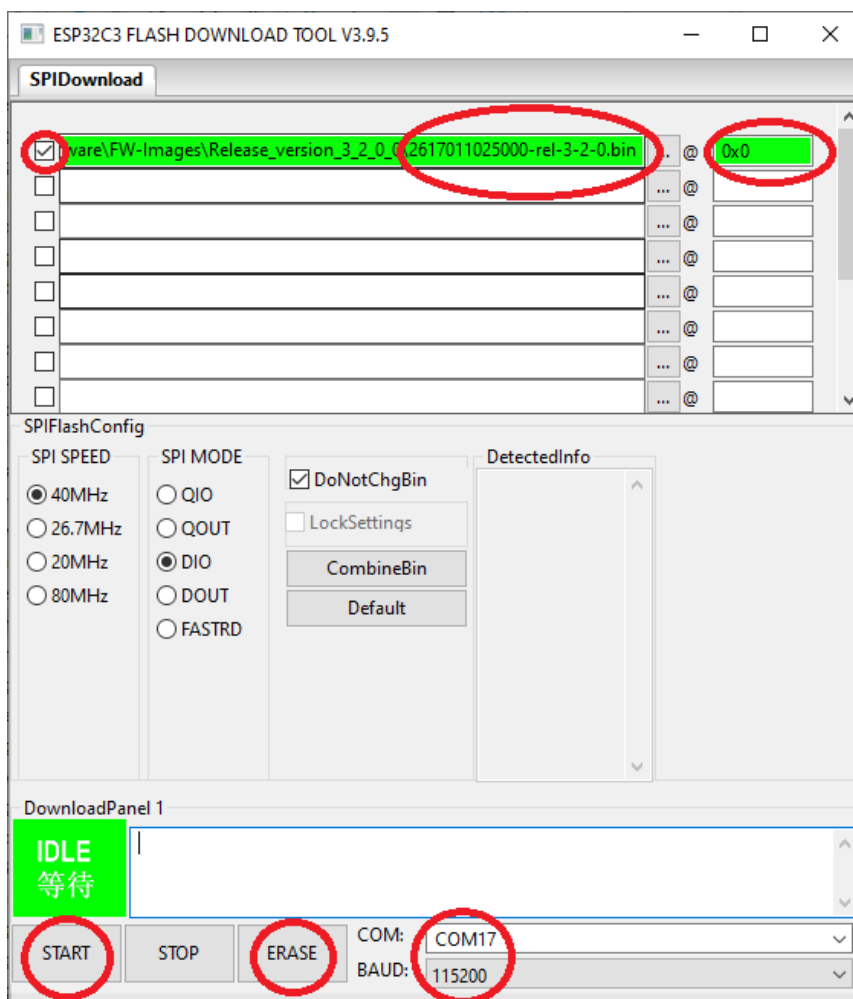


Figure 12: Flash download tool: Erase or flash chipset

9 Manufacturing information

9.1 Moisture sensitivity level

This wireless connectivity product is categorized as JEDEC Moisture Sensitivity Level 3 (MSL3), which requires special handling.

More information regarding the MSL requirements can be found in the IPC/JEDEC J-STD-020 standard on www.jedec.org.

More information about the handling, picking, shipping and the usage of moisture/reflow and/or process sensitive products can be found in the IPC/JEDEC J-STD-033 standard on www.jedec.org.

9.2 Soldering

9.2.1 Reflow soldering

Attention must be paid on the thickness of the solder resist between the host PCB top side and the modules bottom side. Only lead-free assembly is recommended according to JEDEC J-STD020.

Profile feature		Value
Preheat temperature, min	$T_{S \text{ Min}}$	150 °C
Preheat temperature, max	$T_{S \text{ Max}}$	200 °C
Preheat time from $T_{S \text{ Min}}$ to $T_{S \text{ Max}}$	t_S	60 - 120 s
Ramp-up rate (T_L to T_P)		3 °C/s max.
Liquidous temperature	T_L	217 °C
Time t_L maintained above T_L	t_L	60 - 150 s
Peak package body temperature	T_P	260 °C
Time within 5 °C of actual peak temperature	t_P	20 - 30 s
Ramp-down rate (T_P to T_L)		6 °C/s max.
Time 20 °C to T_P		8 min max.

Table 11: Classification reflow soldering profile, Note: refer to IPC/JEDEC J-STD-020E

It is recommended to solder this module on the last reflow cycle of the PCB. For solder paste use a LFM-48W or Indium based SAC 305 alloy (Sn 96.5 / Ag 3.0 / Cu 0.5 / Indium 8.9HF / Type 3 / 89 %) type 3 or higher.

The reflow profile must be adjusted based on the thermal mass of the entire populated PCB, heat transfer efficiency of the reflow oven and the specific type of solder paste used. Based on the specific process and PCB layout the optimal soldering profile must be adjusted and verified. Other soldering methods (e.g. vapor phase) have not been verified and have to be validated by the customer at their own risk. Rework is not recommended.

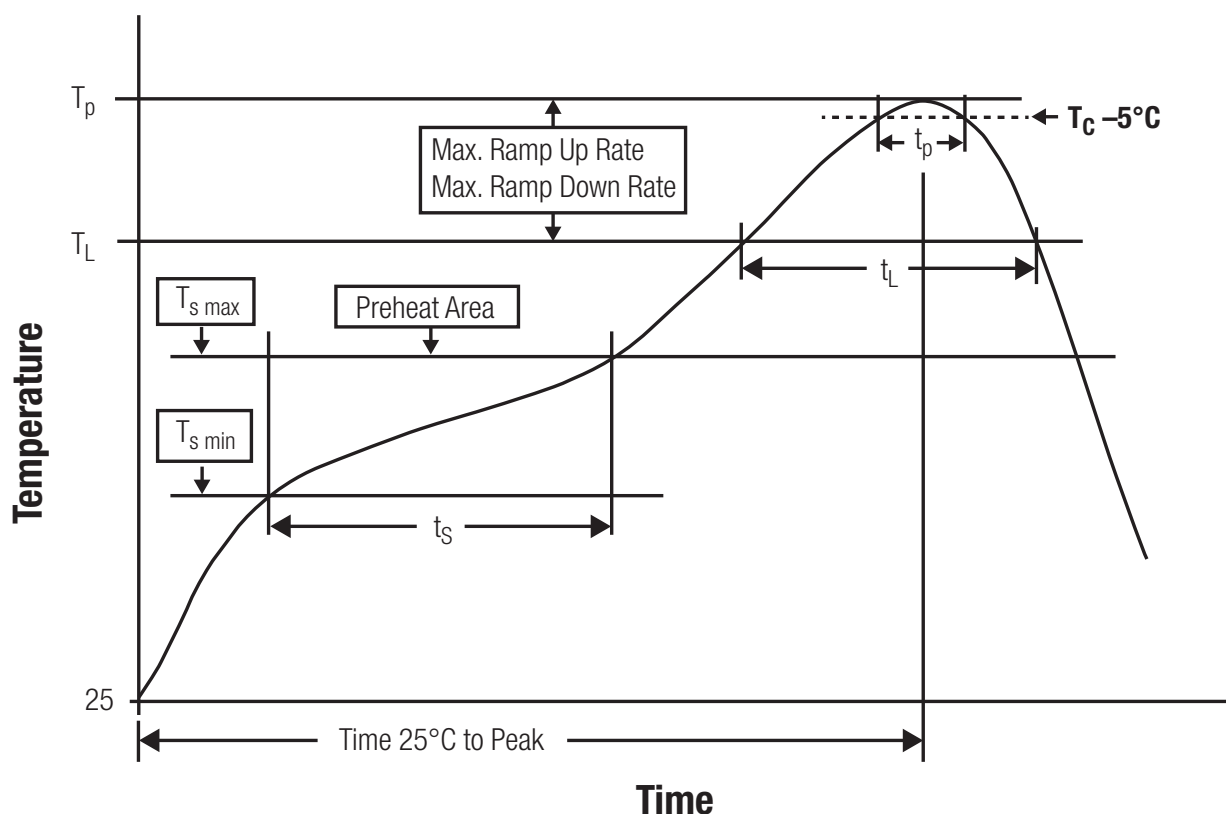


Figure 13: Reflow soldering profile

After reflow soldering, visually inspect the board to confirm proper alignment.

9.2.2 Cleaning

Do not clean the product. Any residue cannot be easily removed by washing. Use a "no clean" soldering paste and do not clean the board after soldering.

- Do not clean the product with water. Capillary effects can draw water into the gap between the host PCB and the module, absorbing water underneath it. If water is trapped inside, it may short-circuit adjoining pads. The water may also destroy the label and ink-jet printed text on it.
- Cleaning processes using alcohol or other organic solvents may draw solder flux residues into the housing, which won't be detected in a post-wash inspection. The solvent may also destroy the label and ink-jet printed text on it.
- Do not use ultrasonic cleaning as it will permanently damage the part, particularly the crystal oscillators.

9.2.3 Potting and coating

- If the product is potted in the customer application, the potting material might shrink or expand during and after hardening. Shrinking could lead to an incomplete seal, allowing contaminants into the component. Expansion could damage components. We recommend a manual inspection after potting to avoid these effects.
- Conformal coating or potting results in loss of warranty.
- The RF shield will not protect the part from low-viscosity coatings and potting. An undefined amount of coating and potting will enter inside the shielding.
- Conformal coating and potting will influence the parts of the radio front end and consequently influence the radio performance.
- Potting will influence the temperature behavior of the device. This might be critical for components with high power.

9.2.4 Other notations

- Do not attempt to improve the grounding by forming metal strips directly to the EMI covers or soldering on ground cables, as it may damage the part and will void the warranty.
- Always solder every pad to the host PCB even if some are unused, to improve the mechanical strength of the module.
- The part is sensitive to ultrasonic waves, as such do not use ultrasonic cleaning, welding or other processing. Any ultrasonic processing will void the warranty.

9.3 ESD handling

This product is highly sensitive to electrostatic discharge (ESD). As such, always use proper ESD precautions when handling. Make sure to handle the part properly throughout all stages of production, including on the host PCB where the module is installed. For ESD ratings, refer to the module series' maximum ESD section. For more information, refer to the relevant chapter 4. Failing to follow the aforementioned recommendations can result in severe damage to the part.

- The first contact point when handling the PCB is always between the local GND and the host PCB GND, unless there is a galvanic coupling between the local GND (for example work table) and the host PCB GND.
- Before assembling an antenna patch, connect the grounds.
- While handling the RF pin, avoid contact with any charged capacitors and be careful when contacting any materials that can develop charges (for example coaxial cable with around 50-80 pF/m, patch antenna with around 10 pF, soldering iron etc.)
- Do not touch any exposed area of the antenna to avoid electrostatic discharge. Do not let the antenna area be touched in a non ESD-safe manner.
- When soldering, use an ESD-safe soldering iron.

9.4 Safety recommendations

It is your duty to ensure that the product is allowed to be used in the destination country and within the required environment. Usage of the product can be dangerous and must be tested and verified by the end user. Be especially careful of:

- Use in areas with risk of explosion (for example oil refineries, gas stations).
- Use in areas such as airports, aircraft, hospitals, etc., where the product may interfere with other electronic components.

It is the customer's responsibility to ensure compliance with all applicable legal, regulatory and safety-related requirements as well as applicable environmental regulations. Disassembling the product is not allowed. Evidence of tampering will void the warranty.

- Compliance with the instructions in the product manual is recommended for correct product set-up.
- The product must be provided with a consolidated voltage source. The wiring must meet all applicable fire and security prevention standards.
- Handle with care. Avoid touching the pins as there could be ESD damage.

Be careful when working with any external components. When in doubt consult the technical documentation and relevant standards. Always use an antenna with the proper characteristics.



Würth Elektronik eiSos radio modules with high output power of up to 500 mW generate a large amount of heat while transmitting. The manufacturer of the end device must take care of potentially necessary actions for his application.

10 Product testing

10.1 Würth Elektronik eiSos in-house production tests

To achieve a high quality standard, Würth Elektronik eiSos follows a philosophy of supplying fully tested radio modules. At the end of the production process, every unit undergoes an optical inspection. Here the quality of soldering, edge castellation and edge milling is monitored.

If this has been passed, the radio modules are handed over to the automatic test equipment for the electrical characterization. This includes:

- Voltage and current tests to ensure proper electrical performance
- RF characteristics (frequency, spectrum, TX power) measurement and calibration
- Radio communication tests
- Firmware and serial number programming
- Host interface communication tests

The automated testing process is logged for internal quality control. The gained measurement data of each unit is analysed to detect defective parts and investigate the corresponding root cause. Defective radio modules are discarded, in order to guarantee a 100% failure-free delivery to customers.

10.2 EMS production tests

The rigorous in-series production testing ensures that EMS don't need to duplicate firmware tests or measurements. This streamlines the process and eliminates the need for additional testing over analogue and digital interfaces during device production. When it comes to device testing, the ideal focus should be on module assembly quality:

- All module pins are soldered properly on the base PCB
- There are no short circuits
- The mounting process did not damage the module
- The communication between host and radio module is working
- The antenna is connected properly

Simple "Go/No go" tests, like checking the RSSI value, give already a hint if the power supply and antenna have been connected properly.

In addition to such standard testing procedures, radio module integrators have the flexibility to perform additional dedicated tests to thoroughly evaluate the device. Specific tests they can consider are:

- Measure module current consumption in a specified operating state. Deviations from expected results (compared to a "Golden Device") can signal potential issues.

- Perform functional tests, including communication checks with the host controller and verification of interfaces.
- Assess fundamental RF characteristics (modulation accuracy, power levels, spectrum). Verify that the device meets expected performance standards.

11 Physical specifications

11.1 Dimensions

Dimensions
9.5 x 13 x 2 mm

Table 12: Dimensions

11.2 Weight

Weight
<1 g

Table 13: Weight

11.3 Module drawing

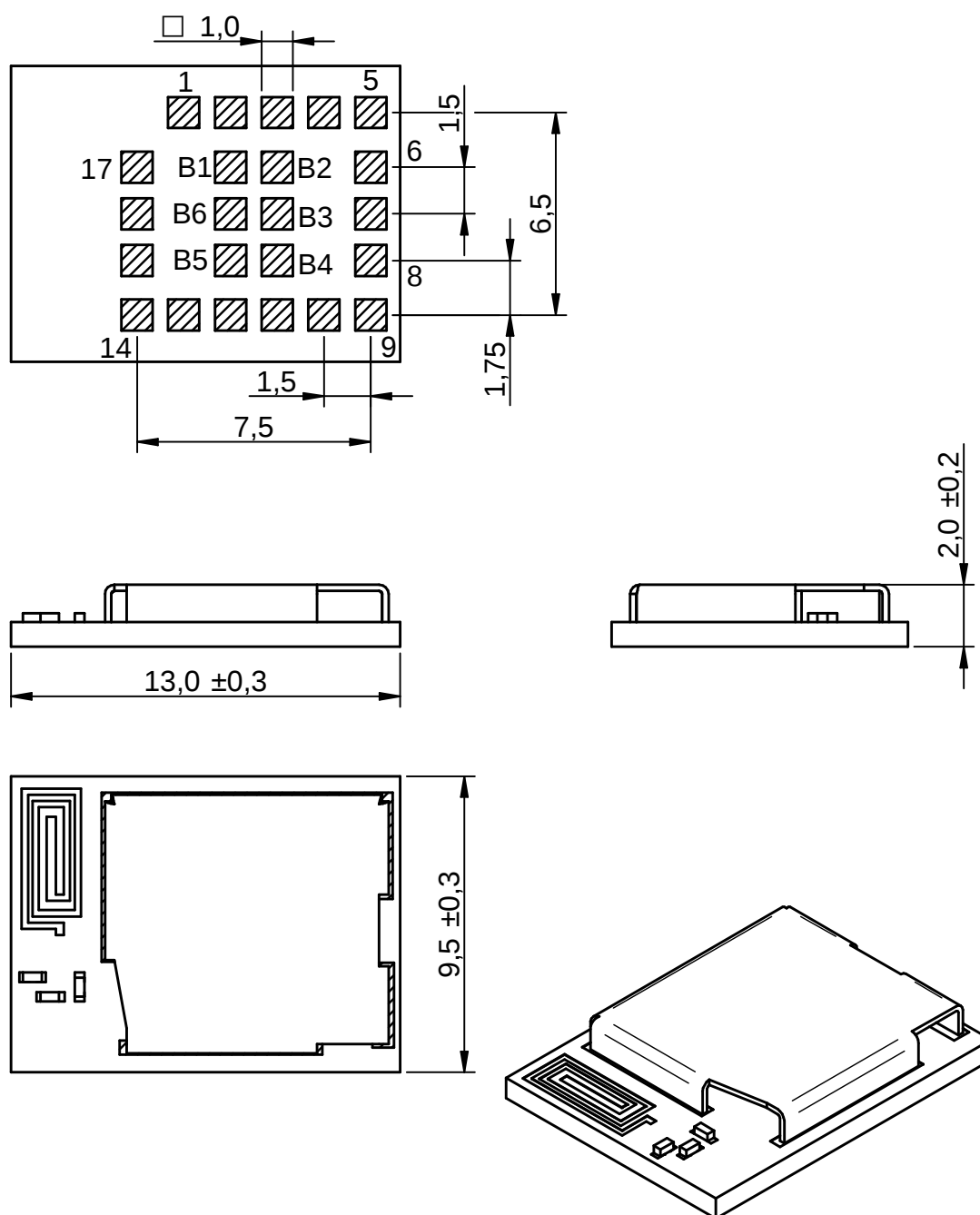


Figure 14: Module dimensions [mm]

11.4 Footprint

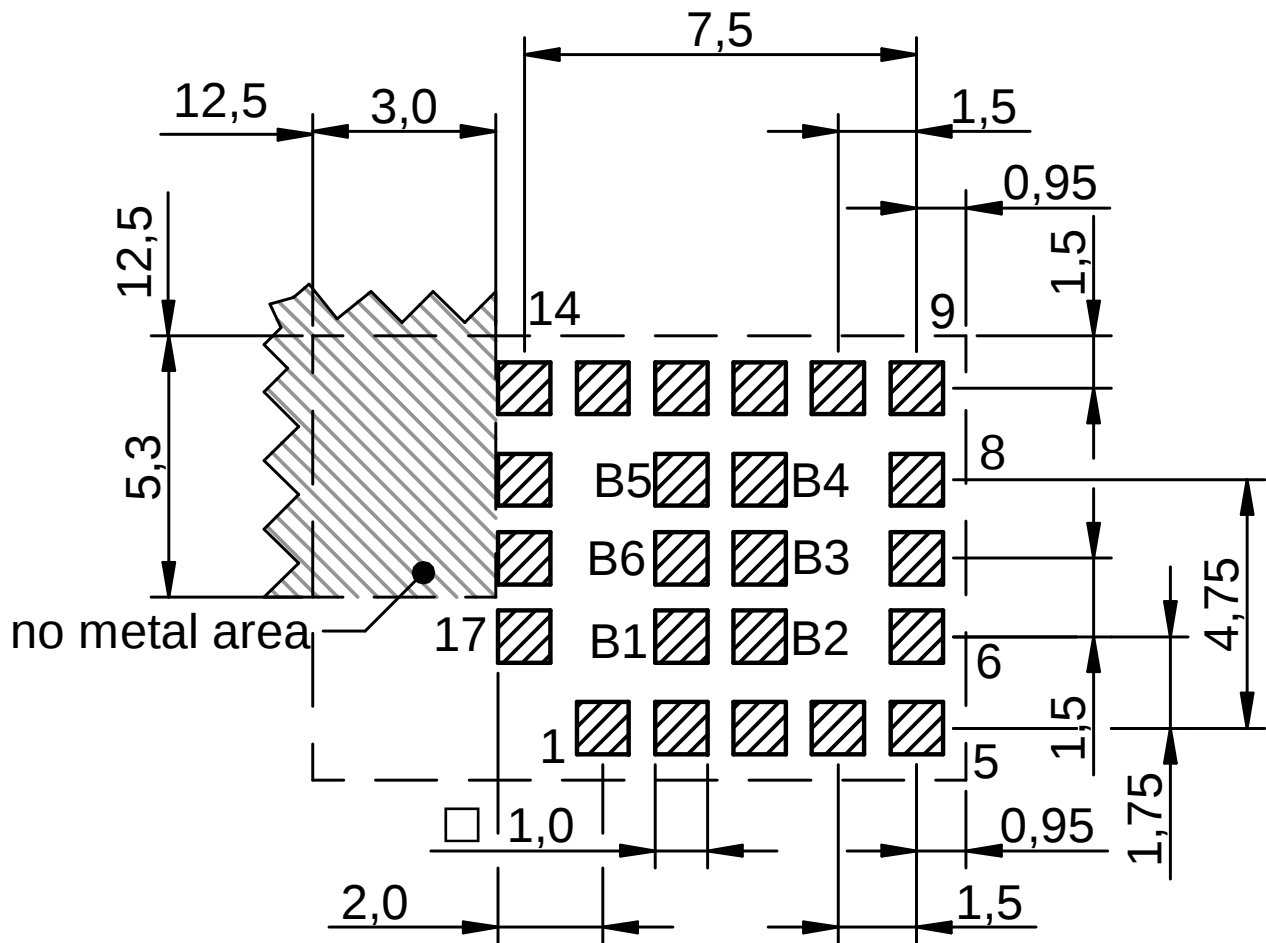


Figure 15: Footprint [mm]

11.5 Antenna free area

To avoid influence and mismatching of the antenna, the recommended free area around the antenna should be maintained. As rule of thumb, a minimum distance of metal parts to the antenna of $\lambda/10$ should be kept (see figure 15). Even though metal parts would influence the characteristic of the antenna, but the direct influence and matching keep an acceptable level.

12 Marking

12.1 Lot number

The 15 digit lot number is printed in numerical digits as well as in form of a machine readable bar code. It is divided into 5 blocks as shown in the following picture and can be translated according to the following table.

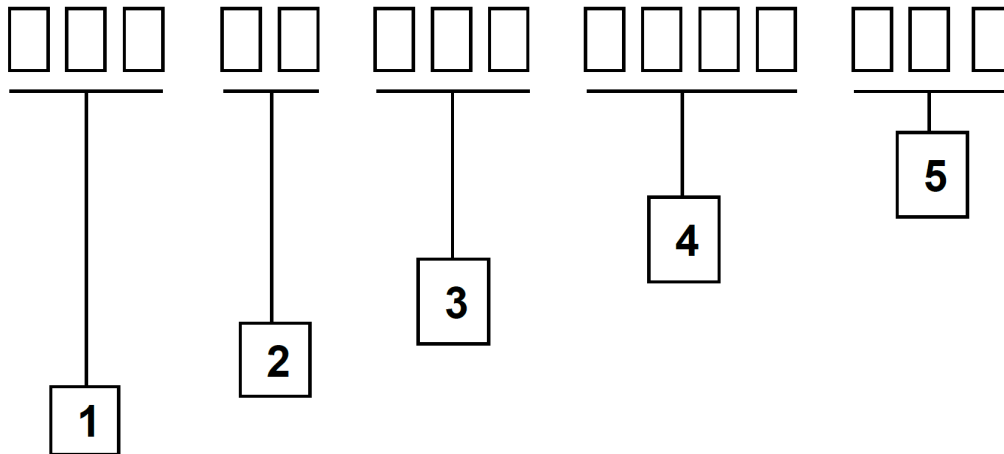


Figure 16: Lot number structure

Block	Information	Example(s)
1	eiSos internal, 3 digits	438
2	eiSos internal, 2 digits	01
3	Radio module hardware version, 3 digits	V2.4 = 024, V12.2 = 122
4	Date code, 4 digits	1703 = week 03 in year 2017, 1816 = week 16 in year 2018
5	Radio module firmware version, 3 digits	V3.2 = 302, V5.13 = 513

Table 14: Lot number details

As the user can perform a firmware update the printed lot number only shows the factory delivery state. The currently installed firmware can be requested from the module using the corresponding product specific command. The firmware version as well as the hardware version are restricted to show only major and minor version not the patch identifier. Block 5 is not applicable for products without firmware.

12.2 General labeling information

Labels of Würth Elektronik eiSos radio modules include several fields. Besides the manufacturer identification, the product's *WE* order code, serial number and certification information are placed on the label. In case of small labels, additional certification marks are placed on the label of the reel.

The information on the label are fixed. Only the serial number changes with each entity of the radio module. For Orthosie-I the label is as follows:

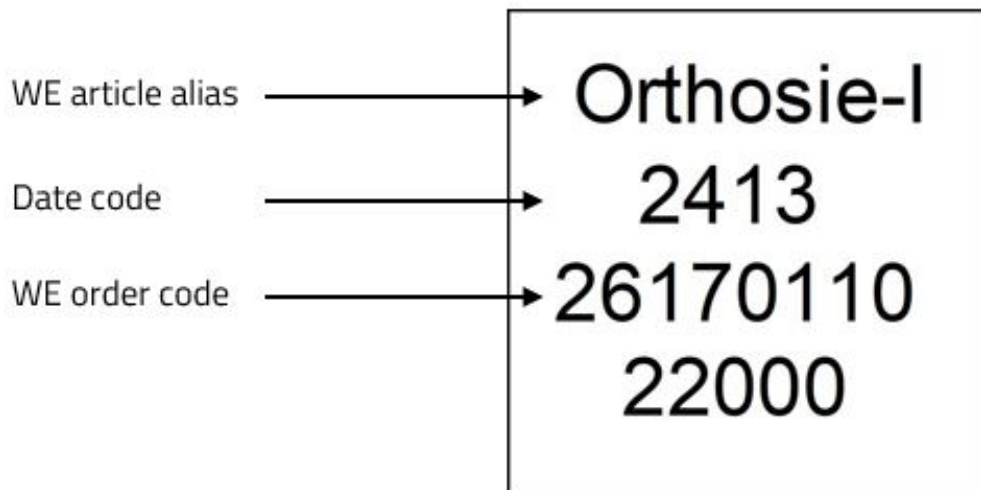


Figure 17: Label of the Orthosie-I

13 Information for explosion protection

In case the end product should be used in explosion protection areas, the following information can be used:

- The module itself is unfused.
- The maximum power of the module is 15 dBm for internal antenna.
- The total amount of capacitance of all capacitors is 3.61 μF .
- The total amount of inductance of all inductors is 27.9 nH.

14 Bluetooth SIG listing/qualification

Type	Data
Design name	Stephano-I
Declaration ID	D066310
QDID	227283 (Controller Subsystem) ¹
Specification name	Bluetooth® LE 5.0
Project type	End product

Each product containing intellectual property of the Bluetooth® Special Interest Group (SIG) must be qualified by the SIG to obtain the corresponding declaration ID.

Due to the qualification as "Controller Subsystem", no further Bluetooth® LE tests are required. The only arising expenses are those for purchasing a Bluetooth® declaration ID.

To obtain the Bluetooth® listing of the end device, refer to the application note ANR027 [5].

¹For listing of the end device, the controller subsystem QDID (227283) must be used in addition to the QDID of the Bluetooth® LE stack. For example the stack "ESP-IDF Bluedroid Host" with QDID 198312 listed as "Host Subsystem" can be used.

15 References

- [1] Espressif. Espressif SDKs. <https://www.espressif.com/en/products/software/esp-sdk/overview>.
- [2] Espressif. Espressif tools download page. <https://www.espressif.com/en/support/download/other-tools>.
- [3] Espressif. Espressif download tool documentation. https://docs.espressif.com/projects/esp-test-tools/en/latest/esp32c3/development_stage/rf_test_guide/rf_test_guide.html#download-configuration-area.
- [4] Espressif. Espressif test tool documentation. https://docs.espressif.com/projects/esp-test-tools/en/latest/esp32c3/development_stage/rf_test_guide/rf_test_guide.html#esprftesttool.
- [5] Würth Elektronik. Application note 27 - Bluetooth listing guide. <http://www.we-online.com/ANR027>.

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User manual Orthosie-I

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